

WEST Search History

DATE: Friday, June 16, 2006

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L69	l61 and L66	1
<input type="checkbox"/>	L68	l61 and L67	0
<input type="checkbox"/>	L67	708/670.ccls.	223
<input type="checkbox"/>	L66	712/221.ccls.	367
<input type="checkbox"/>	L65	(instruction\$1 or L63 or L64) with L61	29
		<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L64	macroinstruction\$1 or macro-instruction\$1 or macrocode or macro-code or macro-operation\$1 or macrooperation\$1 or macro-op\$1 or macroop\$1 or macro-program\$1 or macroprogram\$1 or cisc instruction	1617
<input type="checkbox"/>	L63	firmware or nanoinstruction\$1 or nano-instruction\$1 or nanocode or nano-code or nanoprogram or nano-program or L62	80217
<input type="checkbox"/>	L62	microinstruction\$1 or micro-instruction\$1 or microcode or micro-code or micro-operation\$1 or microoperation\$1 or micro-op\$1 or microop\$1 or micro- program\$1 or microprogram\$1	18085
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L61	add\$5 with packet\$1 with carry	729
<input type="checkbox"/>	L60	L27 with "carry in"	0
		<i>DB=USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L59	L27 with "carry in"	0
<input type="checkbox"/>	L58	(atm or packet\$1) and L57	63
<input type="checkbox"/>	L57	(add or added or adding or addition or adder) with (immediate adj (operand\$1 or value\$1))	239
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L56	(atm or packet\$1) and L55	100
<input type="checkbox"/>	L55	(add or added or adding or addition or adder) with (immediate adj (operand\$1 or value\$1))	377
		<i>DB=USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L54	US-5210835-A.did.	1
<input type="checkbox"/>	L53	US-4941085-A.did.	1
		<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L52	(add or added or adding or addition or adder) and (immediate adj (operand\$1 or value\$1))	29
<input type="checkbox"/>	L51	(add or added or adding) and (immediate adj (operand\$1 or value\$1))	21

<input type="checkbox"/>	L50 (add or added or adding) with (immediate adj (operand\$1 or value\$1)) <i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	7
<input type="checkbox"/>	L49 packet\$1 and L48	1
<input type="checkbox"/>	L48 atm and L47	3
<input type="checkbox"/>	L47 (add or added or adding) with (immediate adj (operand\$1 or value\$1))	300
<input type="checkbox"/>	L46 L45 not L34	81
<input type="checkbox"/>	L45 L27 with (immediate or constant\$1)	342
<input type="checkbox"/>	L44 packet\$1 and L43	24
<input type="checkbox"/>	L43 start bit\$1 with operand\$1	33
<input type="checkbox"/>	L42 L30 with operand\$1	0
<input type="checkbox"/>	L41 L37 with operand\$1	5
<input type="checkbox"/>	L40 L38 and L39	99
<input type="checkbox"/>	L39 L35 same L27	12119
<input type="checkbox"/>	L38 L35 and L37	99
<input type="checkbox"/>	L37 marker bit\$1	864
<input type="checkbox"/>	L36 L30 and L35	5
<input type="checkbox"/>	L35 L27 not L34	12119
<input type="checkbox"/>	L34 packet with address\$3	32038
<input type="checkbox"/>	L33 L27 same L30	1
<input type="checkbox"/>	L32 L27 and L30	39
<input type="checkbox"/>	L31 L28 and L30	0
<input type="checkbox"/>	L30 bit marker\$1	250
<input type="checkbox"/>	L29 start bit\$1 and L28	1
<input type="checkbox"/>	L28 operand\$1 with L27	86
<input type="checkbox"/>	L27 packet with add\$5	41400
<input type="checkbox"/>	L26 baum.in. and apple.asn.	4
<input type="checkbox"/>	L25 baum.in. and apple.asn. and multi-guage.ti.	0
<input type="checkbox"/>	L24 6047304.pn. <i>DB=USPT; PLUR=NO; OP=ADJ</i>	1
<input type="checkbox"/>	L23 bit field arithmetic	2
<input type="checkbox"/>	L22 120 and L19	1
<input type="checkbox"/>	L21 (add or added or adding or adder or adds) and L19	3
<input type="checkbox"/>	L20 (add or added or adding or adder or adds) same L19	0
<input type="checkbox"/>	L19 6047304.uref.	3
<input type="checkbox"/>	L18 (add or added or adding or adder or adds) same L10	3
<input type="checkbox"/>	L17 (add or added or adding or adder or adds) same L11	1
<input type="checkbox"/>	L16 L11 not L15	25
<input type="checkbox"/>	L15 L11 not L14	69

☐	L14	packet\$1 and L11	25
☐	L13	214 and L12	1
☐	L12	packet\$1 same L11	1
☐	L11	dedicated logic circuitry	94
☐	L10	dedicated logic device\$1	23
☐	L9	packet\$1 same L8	1
☐	L8	L7	718
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
☐	L7	dedicated with logic device\$1	1343
		<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>	
☐	L6	(carry save add\$2) with instruction\$1	4
☐	L5	L4	0
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
☐	L4	(carry save add\$2) with instruction\$1	33
☐	L3	carry save add\$2 instruction\$1	0
☐	L2	carry save add instruction	0
☐	L1	5838960.pn.	1

END OF SEARCH HISTORY